SC3-LARGO • Resources w. 1+8 Slots Backplane (System Slot Left)

Territory: SC3-LARGO

**P6**
- **GbE** (1-2)

**P5**
- **GbE** (1)
- **GbE** (2)
- **Clk PE** (1-8)
- **2 x PE** (7-8)
- **Gen2**
- **2 Links x 1**

**P4**
- **4 x PE** (3-6)
- **Gen2**
- **4 Links x 1**
- **PER**
  - **1**
  - **2**
  - **3**
  - **4**

**P3**
- **3 x SATA (PCH)**
- **4 x SATA (RAID)**
- **1 x USB3 (2)**
- **2 x USB2 (5-6)**
- **PE Gen3 x 8** (2)
- **PE Gen3**
  - **½ x 8 (1)**
  - **2 x USB2 (3-4)**
  - **4**

**P2**
- **PE Gen3**
  - **½ x 8 (1)**
  - **1 x USB3 (1)**
  - **Sideband**

**P1**
- **SC3-LARGO**
- **Fat Pipe Slot**
- **Fat Pipe Slot**
- **Peripheral Slot**
- **Peripheral Slot**
- **Peripheral Slot**
- **Peripheral Slot**
- **Peripheral Slot**
- **Peripheral Slot**

**Connectors Assignment Numbers**

- **SYS**
  - **P1**
  - **P2**
  - **P3**
  - **P4**
  - **P5**
  - **P6**

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*System slot connector assignment numbers in brackets (e.g. SATA PCH (7-8) according to the CPCI-S.0 specification table 44/45)

- SATA (PCH) assigned connectors are Intel QM87 Platform Controller Hub derived ports
- SATA (RAID) assigned connectors are Marvell 88SE9230 hardware RAID controller derived ports (may be operated non RAID)*
### SC3-LARGO • Resources w. 1+8 Slots Backplane (System Slot Right)

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<th>Resources</th>
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<tr>
<td>P5</td>
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<td>4, 5</td>
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<td>P2</td>
<td>7</td>
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<td>8</td>
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</tbody>
</table>

**Peripheral Slot**
- **P1**: PE Gen2 x 1 SATA 6G (PCH)
- **P2**: PE Gen2 x 1 SATA 6G (RAID) USB2
- **P3**: PE Gen2 x 1 SATA 6G (RAID) USB2
- **P4**: PE Gen2 x 1 SATA 6G (RAID) USB2
- **P5**: PE Gen2 x 1 SATA 6G (RAID) USB2
- **P6**: PE Gen2 x 1 SATA 6G (RAID) USB2
- **P7**: PE Gen3 x 8 SATA 6G (PCH) USB3
- **P8**: PE Gen3 x 8 SATA 6G (PCH) USB3

**GbE**
- **P7**: (2)
- **P8**: (1)

**Fat Pipe Slot**
- **P7**: 2 x PE (7-8) Gen2 2 Links x 1
- **P8**: 4 x PE (3-6) Gen2 4 Links x 1

**Fat Pipe Slot**
- **P7**: 3 x SATA (PCH) 4 x SATA (RAID) 1 x USB3 (2) 2 x USB2 (3-4) I/O
- **P8**: PE Gen3 x 8 (2) PE Gen3 ½ x 8 (1) 2 x USB2 (3-4) I/O

**Sideband**
- **P7**: PE Gen3 ½ x 8 (1) 1 x USB3 (1) Sideband

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*system slot connector assignment numbers in brackets (e.g. SATA PCH (7-8) according to the CPCI-S.0 specification table 44/45
SATA (PCH) assigned connectors are Intel QM87 Platform Controller Hub derived ports
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SC3-LARGO • Resources w. 1+4 Slots Backplane (System Slot Left)

System slot connector assignment numbers in brackets (e.g. SATA PCH (7-8) according to the CPCI-S.0 specification table 44/45

core SATA (PCH) assigned connectors are Intel QM87 Platform Controller Hub derived ports
core SATA (RAID) assigned connectors are Marvell 88SE9230 hardware RAID controller derived ports (may be operated non RAID)

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## SC3-LARGO • Resources w. 1+4 Slots Backplane (System Slot Right)

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### System Slot Connector Assignment Numbers
- **GA 000**: General Purpose Slot
- **GA 001**: General Purpose Slot
- **GA 010**: General Purpose Slot
- **GA 011**: General Purpose Slot

### System Slot Ports
- **GbE (2)**: Gigabit Ethernet
- **GbE (1)**: Gigabit Ethernet
- **GbE (1-2)**: Gigabit Ethernet
- **GbE (1)**: Gigabit Ethernet
- **GbE (1-2)**: Gigabit Ethernet

### Peripheral Slot
- **PE Gen2 x 1 SATA 6G (PCH) USB2**: Peripheral Expansion Slot
- **PE Gen2 x 1 SATA 6G (PCH) USB2**: Peripheral Expansion Slot
- **PE Gen3 x 8 SATA 6G (RAID) USB3**: Fat Pipe Slot
- **PE Gen3 x 8 SATA 6G (RAID) USB3**: Fat Pipe Slot
- **PE Gen3 x 8 (2)**: PE Gen3
- **PE Gen3 ½ x 8 (1)**: PE Gen3
- **PE Gen3 ½ x 8 (1)**: PE Gen3
- **SC3-LARGO Sideband**: SC3-LARGO Sideband

### Notes
- System slot connector assignment numbers in brackets (e.g. SATA PCH (7-8) according to the CPCI-S.0 specification table 44/45)
- SATA (PCH) assigned connectors are Intel QM87 Platform Controller Hub derived ports
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